

CLAIMS

We Claim:

- 1 1. A lead frame comprising:
2 pins for a plurality of parts, the pins comprising:
3 first pins for a first part, the first pins for the first part including:
4 first shaped pins, each of the first shaped pins having a wide
5 area of a first length, and a narrow area, and
6 second shaped pins, each of the second shaped pins having a
7 wide area of a second length and a narrow area, wherein the first length and the
8 second length are not equal, and
9 first pins for a second part;
10 wherein the first pins for the first part are interdigitated with the first
11 pins for the second part.

- 1 2. A lead frame as in claim 1 wherein the first pins for the second part
2 include:
3 first shaped pins for the second part, each of the first shaped pins for the
4 second part having a wide area of the first length, and a narrow area; and,
5 second shaped pins for the second part, each of the second shaped pins for
6 the second part having a wide area of the second length and a narrow area.

- 1 3. A lead frame as in claim 2:
2 wherein the first length is longer than the second length; and,

3 wherein the first pins for the first part are interdigitated with the first
4 pins for the second part so that none of the first shaped pins for the first part are
5 immediately adjacent to any of the first shaped pins for the second part.

1 4. A lead frame as in claim 1 wherein the first length is longer than the
2 second length and the first shaped pins have lesser inductance than the second
3 shaped pins.

1 5. A lead frame as in claim 1, wherein the pins for the plurality of parts
2 additionally comprise:
3 second pins for the first part; and,
4 first pins for a third part;
5 wherein the second pins for the first part are interdigitated with the first
6 pins for the third part.

1 6. A lead frame as in claim 5 wherein the second pins for the first part
2 include:
3 third shaped pins for the first part, each of the third shaped pins for the
4 first part having a wide area of the first length, and a narrow area; and,
5 fourth shaped pins for the first part, each of the fourth shaped pins for the
6 first part having a wide area of the second length and a narrow area.

1 7. A lead frame as in claim 6 wherein the first pins for the third part
2 include:
3 first shaped pins for the third part, each of the first shaped pins for the
4 third part having a wide area of the first length, and a narrow area; and,
5 second shaped pins for the third part, each of the second shaped pins for
6 the third part having a wide area of the second length and a narrow area.

1 8. A lead frame as in claim 7:
2 wherein the first length is longer than the second length; and,
3 wherein the second pins for the first part are interdigitated with the first
4 pins for the third part so that none of the third shaped pins for the first part are
5 immediately adjacent to any of the first shaped pins for the third part.

1 9. A lead frame as in claim 5, wherein the pins for the plurality of parts
2 additionally comprise:
3 second pins for the second part; and,
4 first pins for a fourth part;
5 wherein the second pins for the second part are interdigitated with the
6 first pins for the fourth part.

1 10. A method for constructing a lead frame comprising:
2 forming pins for a plurality of parts, including the following;
3 forming first pins for a first part, including:

4 forming first shaped pins, each of the first shaped pins
5 having a wide area of a first length, and a narrow area, and
6 forming second shaped pins, each of the second shaped pins
7 having a wide area of a second length and a narrow area, wherein the first
8 length and the second length are not equal, and
9 forming first pins for a second part, wherein the first pins for the
10 first part are interdigitated with the first pins for the second part.

1 11. A method as in claim 10 wherein forming the first pins for the second
2 part include:
3 forming first shaped pins for the second part, each of the first shaped pins
4 for the second part having a wide area of the first length, and a narrow area; and,
5 forming second shaped pins for the second part, each of the second shaped
6 pins for the second part having a wide area of the second length and a narrow
7 area.

1 12. A method as in claim 10:
2 wherein the first length is longer than the second length; and,
3 wherein the first pins for the first part are interdigitated with the first
4 pins for the second part so that none of the first shaped pins for the first part are
5 immediately adjacent to any of the first shaped pins for the second part.

1 13. A method as in claim 10 wherein the first length is longer than the
2 second length and the first shaped pins have lesser inductance than the second
3 shaped pins

1 14. A method as in claim 10, wherein forming the pins for the plurality of
2 parts additionally comprises:
3 forming second pins for the first part; and,
4 forming first pins for a third part;
5 wherein the second pins for the first part are interdigitated with the first
6 pins for the third part.

1 15. A method as in claim 14 wherein forming the second pins for the first
2 part includes:
3 forming third shaped pins for the first part, each of the third shaped pins
4 for the first part having a wide area of the first length, and a narrow area; and,
5 forming fourth shaped pins for the first part, each of the fourth shaped
6 pins for the first part having a wide area of the second length and a narrow area.

1 16. A method as in claim 15 wherein forming the first pins for the third
2 part include:
3 forming first shaped pins for the third part, each of the first shaped pins
4 for the third part having a wide area of the first length, and a narrow area; and,

5 forming second shaped pins for the third part, each of the second shaped
6 pins for the third part having a wide area of the second length and a narrow
7 area.

1 17. A method as in claim 16:
2 wherein the first length is longer than the second length; and,
3 wherein the second pins for the first part are interdigitated with the first
4 pins for the third part so that none of the third shaped pins for the first part are
5 immediately adjacent to any of the first shaped pins for the third part.

1 18. A method as in claim 14, wherein forming the pins for the plurality of
2 parts additionally comprise:
3 forming second pins for the second part; and,
4 forming first pins for a fourth part;
5 wherein the second pins for the second part are interdigitated with the
6 first pins for the fourth part.

1 19. An integrated circuit part comprising:
2 a plurality of pins, including:
3 first shaped pins, each of the first shaped pins having a wide area of
4 a first length, and a narrow area, and

5 second shaped pins, each of the second shaped pins having a wide
6 area of a second length and a narrow area, wherein the first length and the
7 second length are not equal and the inductance of the pins is different.

1 20. An integrated circuit part as in claim 19 wherein the first length is
2 longer than the second length and the first shaped pins have lesser inductance
3 than the second shaped pins.